

100V N-Channel Power MOSFET

DESCRIPTION

The BLM30N10L uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

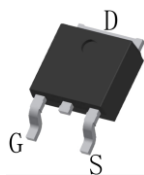
Application

- Power switching application
- Hard switched and High frequency circuits
- Uninterruptible power supply

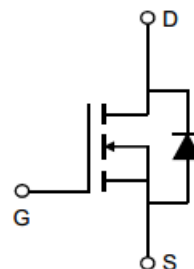
KEY CHARACTERISTICS

- $V_{DS} = 100V, I_D = 25A$
 $R_{DS(ON)} < 30m\Omega @ V_{GS}=10V$
- High density cell design for lower $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high EAS
- Excellent package for good heat dissipation

100% UIS TESTED !
100% DVDS TESTED !



TO-252-2L Top View



Schematic diagram

Package Marking And Ordering Information

Device Marking	Ordering Codes	Package	Product Code	Packing
M30N10L	BLM30N10L-D	TO-252	BLM30N10L	Reel

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	25	A
Drain Current-Pulsed (Note 1)	I_{DM}	100	A
Maximum Power Dissipation ($T_c=25^\circ C$)	P_D	70	W
Single pulse avalanche energy (Note 2)	E_{AS}	96	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.5	$^{\circ}\text{C/W}$
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Electrical Characteristics (TA=25°C unless otherwise noted)

Electrical Characteristics (TA=25°C unless otherwise noted)						
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.8	2.4	V
Drain-Source On-State Resistance ^(Note 3)	R _{DS(ON)}	V _{GS} =10V, I _D =12A	-	25	30	mΩ
		V _{GS} =4.5V, I _D =12A		35	45	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =15A	-	11	-	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	2350	-	pF
Output Capacitance	C _{oss}		-	220	-	pF
Reverse Transfer Capacitance	C _{rss}		-	200	-	pF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, I _D =20A, V _{GS} =10V, R _{GEN} =10Ω	-	29	-	nS
Turn-on Rise Time	t _r		-	13	-	nS
Turn-Off Delay Time	t _{d(off)}		-	58.2	-	nS
Turn-Off Fall Time	t _f			13.4	-	nS
Total Gate Charge	Q _g	V _{DS} =80V, I _D =20A V _{GS} =10V	-	55	-	nC
Gate-Source Charge	Q _{gs}		-	15	-	nC
Gate-Drain Charge	Q _{gd}		-	20	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A	-	-	1.2	V
Reverse Recovery Time	T _{rr}	T _j =25°C , IF=10A, di/dt=100A/uS ^(note3)	-	58	-	nS
Reverse Recovery Charge	Q _{rr}		-	110	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. E_{AS} condition : $T_j=25^{\circ}\text{C}, V_{DD}=50V, V_{GS}=10V, L=0.5mH, R_g=25\Omega$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production.

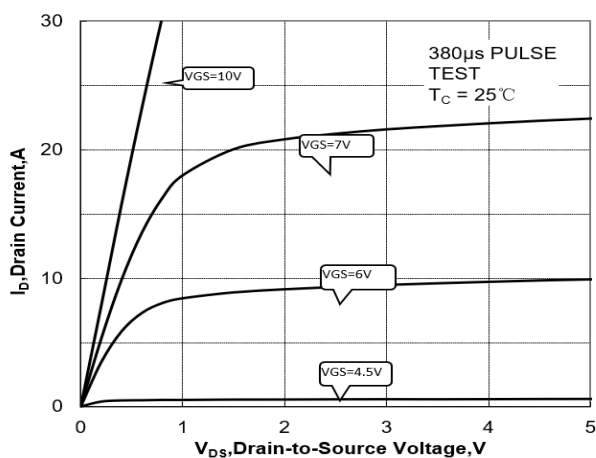
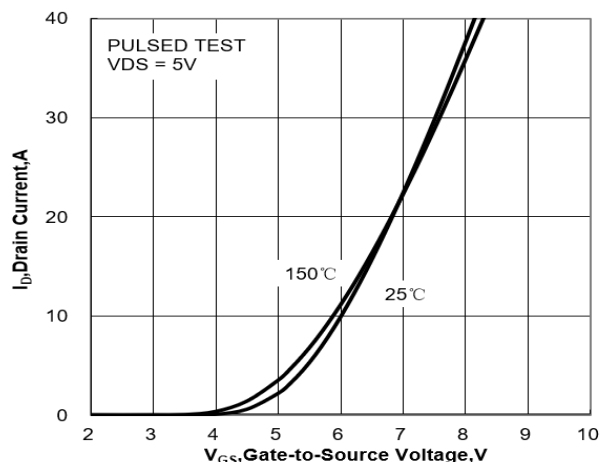
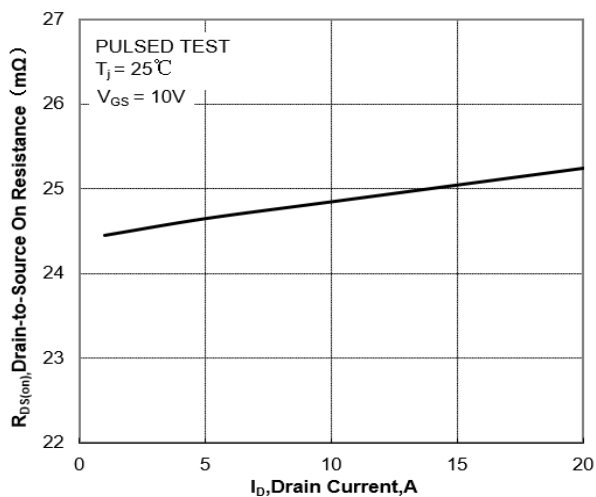
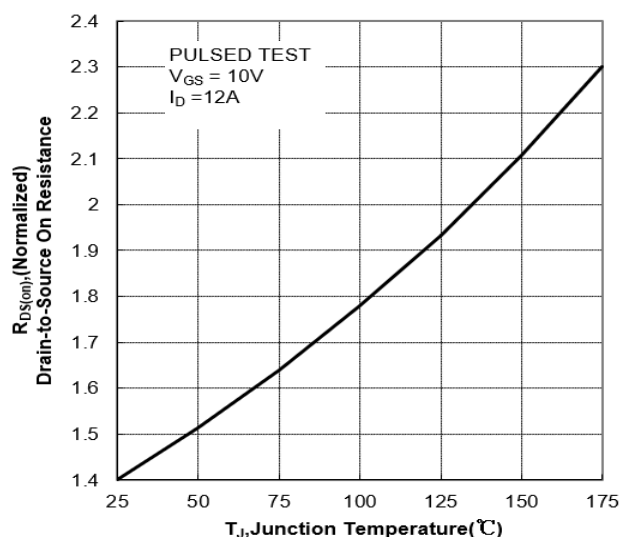
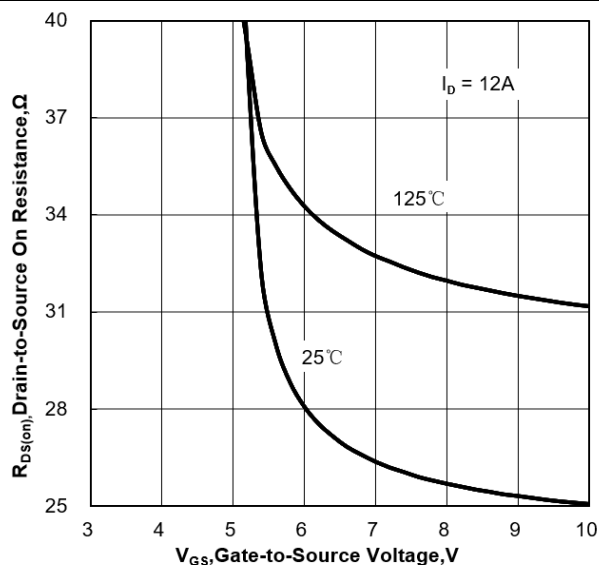
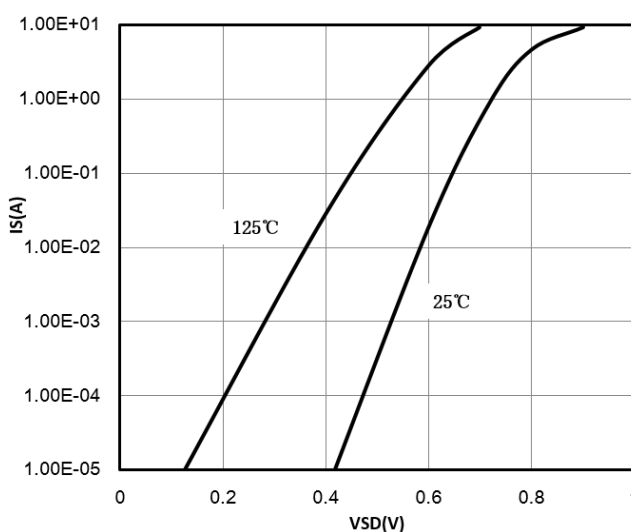
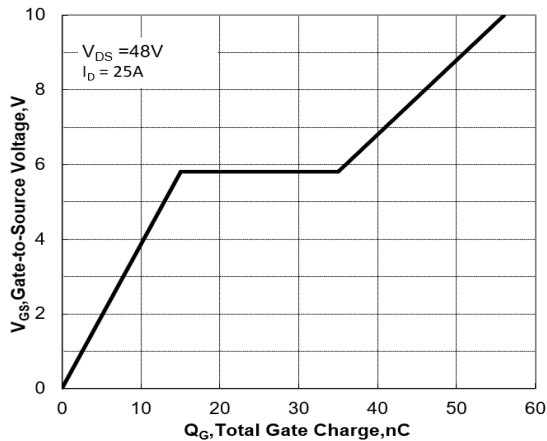
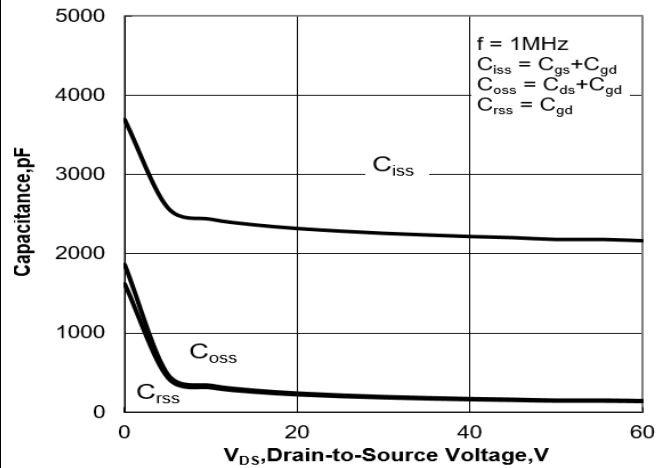
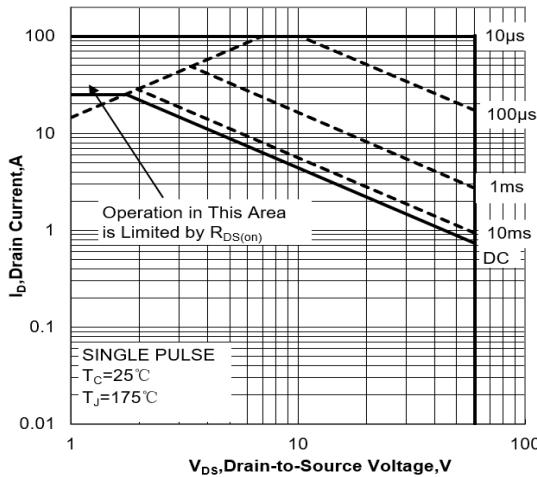
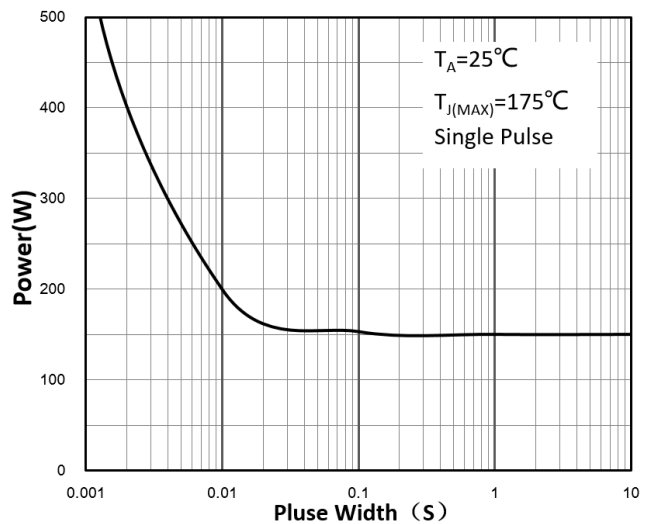
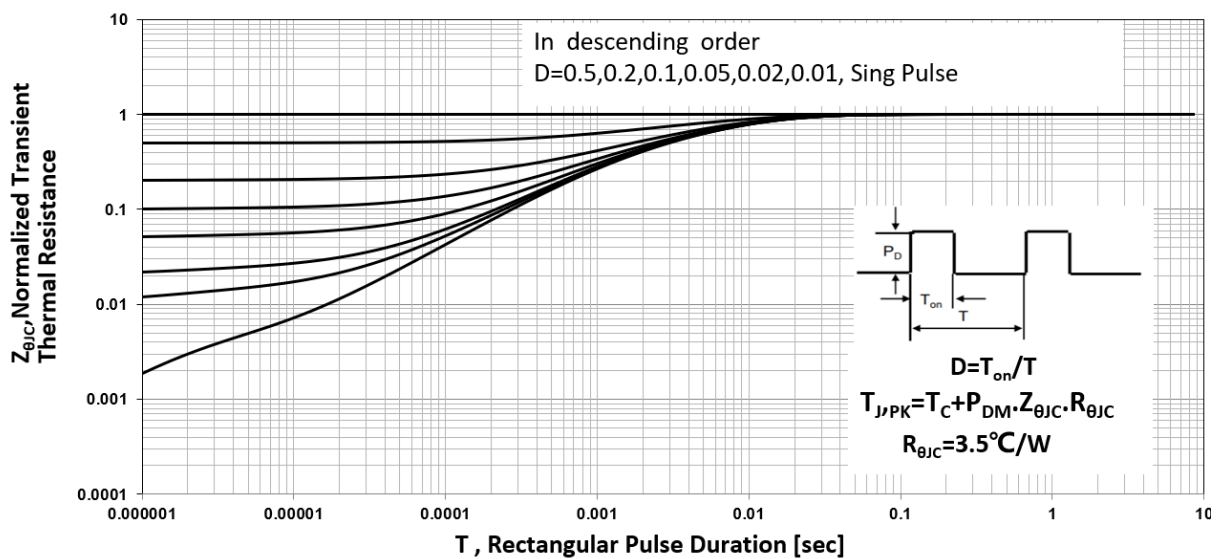
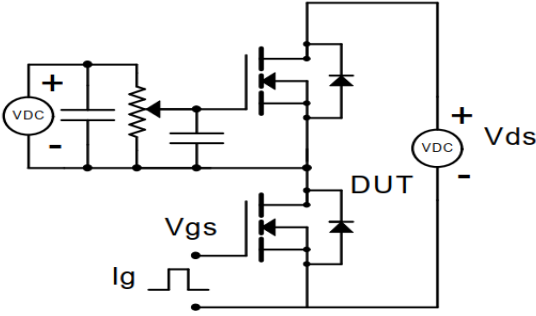
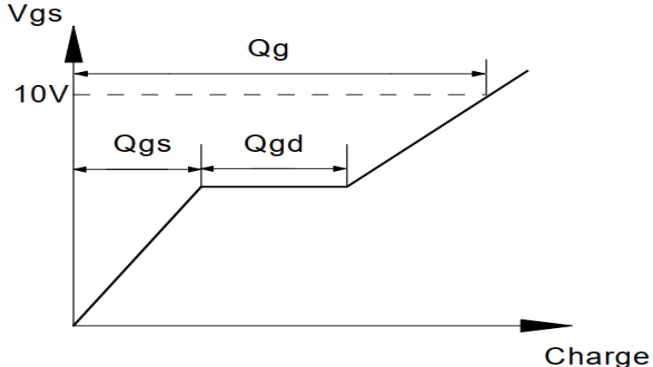
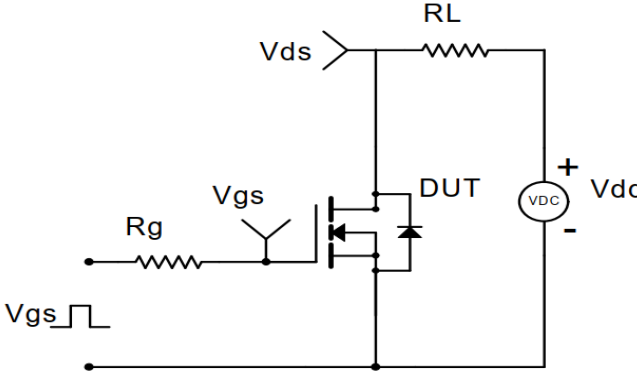
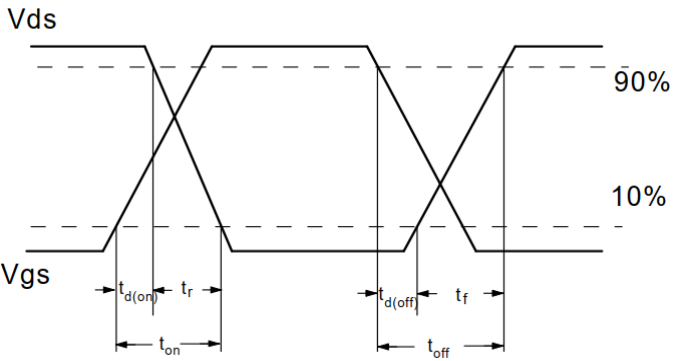
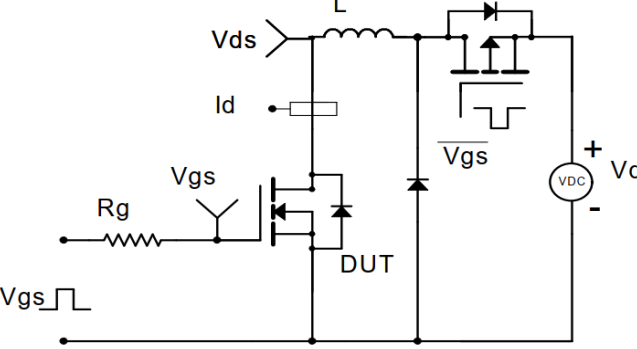
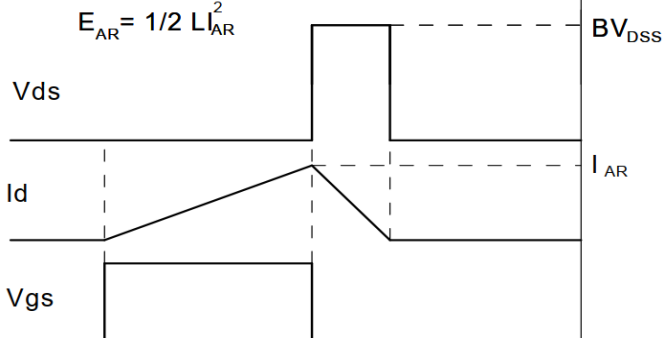
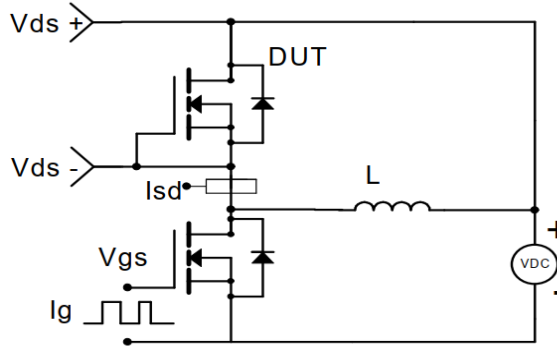
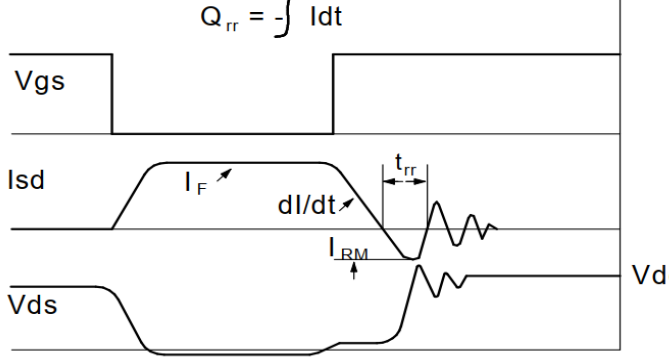
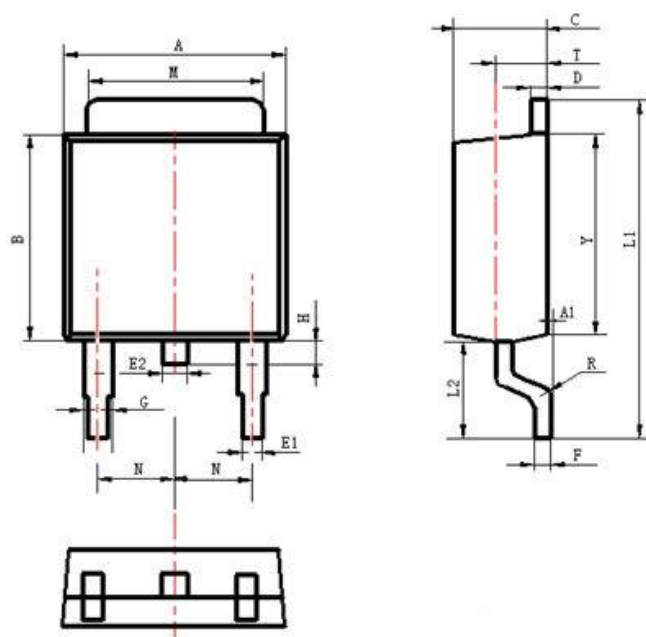
Characteristics Curves
Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 On-Resistance vs. I_D and V_{GS}

Figure 4 On-Resistance vs. Junction Temperature

Figure 5 On-Resistance vs. V_{GS}

Figure 6 Body Diode Forward Voltage


Figure 7 Gate-Charge Characteristics

Figure 8 Capacitance Characteristics

Figure 9 Maximum Forward Biased Safe Operation Area

Figure 10 Single Pulse Power Rating Junction-to-Ambient

Figure 11 Normalized Maximum Transient Thermal Impedance


Test Circuit and Waveform

Gate Charge Test Circuit	Gate Charge Test Waveform
	
Resistive Switching Test Circuit	Resistive Switching Test Waveforms
	
Unclamped Inductive Switching (UIS) Test Circuit	Unclamped Inductive Switching (UIS) Test Waveforms
	
Diode Recovery Test Circuit	Diode Recovery Test Waveforms
	

Package Description



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.60
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shanghai Belling reserves the right to make changes in this specification sheet and is subject to change without prior notice.

CONTACT:**上海贝岭股份有限公司（总部）**

地址：上海市宜山路 810 号

邮编：200233

电话：021-24261000

产品业务咨询及技术支持

电话：021-24261326

传真 2：021-64852222

邮箱 2：marketing@belling.com.cn

上海贝岭深圳分公司（华南区）

地址：深圳市福田区中心区民田路新华保险大厦 1510 室

邮编：518031

电话：0755-33336776 0755-33336770

传真：0755-33336788

上海贝岭北京办事处（华北区）

地址：北京市西城区新华里 16 号院锦官苑小区 10 号楼 1 单元 1505 室

邮编：100044

电话：010-64179374

传真：010-8835 9236